



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Sadayoshi HORII et al.

ATTN: Application Branch

Application No.: 10/529,466

Docket No.: 123373

Filed: May 2, 2005

For:

MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE AND

SUBSTRATE PROCESSING APPARATUS

CORRECTED FILING RECEIPT STATUS REQUEST (SECOND REQUEST)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The above-identified application satisfied the requirements of 35 U.S.C. §371 on May 2, 2005, and was accorded U.S. Application No. 10/529,466. Applicants filed a Request for Correction of PALM Records on October 17, 2005 to reflect the May 2, 2005 §371(c) date and to correct the title (copy enclosed). However, Applicants to date have not received a Corrected Official Filing Receipt.

Applicants hereby request that a Corrected Official Filing Receipt be promptly forwarded to Applicants' representative.

Respectfully submitted,

James A. Oliff

Registration No. 27,075

Kristin K. Vidovich Registration No. 41,448

JAO:KKV/kam

Date: November 15, 2006

OLIFF & BERRIDGE, PLC

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PTO RECEIPT FOR FILING OF PAPERS

► Mail Room (Regular Delivery)

The following papers have been filed:

Request for Correction of PALM Records; Marked-up OFR; Prelim

Name of Applicant: Sadayoshi HORII et al.

Serial No.: 10/529,466

Atty. File No.: 123373

Title (New Cases):

Sender's Initials: JAO/yxm

PATENT OFFICE DATE STAMP



40/B

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SUBSTRATE PROCESSING APPARATUS

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

James A. Oliff Registration No. 27,075

Jesse O. Collier Registration No. 53,839

JAO:JOC/yxm

Date: October 17, 2005

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DEPOSIT ACCOUNT USE **AUTHORIZATION** Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461





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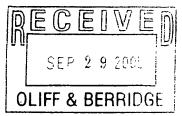
IND CLMS TOT CLMS DRAWINGS ATTY. DOCKET NO FILING OR 371 FIL FEE REC'D ART UNIT APPL NO. 2 (c) DATE 11 11 123373 900 2812 -03/29/2005-

10/529,466

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5/2/2005

OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320



CONFIRMATION NO. 8082

FILING RECEIPT

OC000000017086170

Date Mailed: 09/28/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Sadayoshi Horii, Tokyo, JAPAN; Hironobu Miya, Tokyo, JAPAN;

Assignment For Published Patent Application

Hitachi Kokusai Electronic, Inc., Nakano-ku, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 25944.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/13606 10/24/2003

Foreign Applications

JAPAN 2002-316154 10/30/2002

Projected Publication Date: 12/29/2005

Non-Publication Request: No

Early Publication Request: No

Title

BEST AVAILABLE CO"



for-manufaturing-semiconductor device and substrate processing system

PANUFACTURING METHOD OF SEMICONDUCTOR , EVICED AND ECHOLICATE Preliminary Class

PROCESSING APPARATION 438

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Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international a specific scaling of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same application under the rational patent application in each PCT-member country. The PCT process simplifies the filing of patent applications on the same invention in member countries, but does not result in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

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